Indira Gandhi Delhi Technical University For Women
(Established by Govt. of Delhi vide Act 09 of 2012)
Department of Electronics and Communication Engineering

Report on the Two days Student workshop conducted on Cadence Virtuoso Software from 20-21 August, 2019 in the VLSI Lab, IGDTUW campus.

Department of Electronics and Communication along with IEEE IGDTUW Student Branch and SMDP C2SD at IGDTUW organized a Two days student workshop on Cadence Virtuoso Software from 20-21 August, 2019 in the VLSI Lab, IGDTUW campus.

In this workshop the students were given an introduction of Analog CMOS Design flow. The workshop gave the students an insight about the basics of MOSFETs and various tools used for verification. The participants of the workshop got an overview on analog design analysis and simulation, low power and nanoscale VLSI design.

To excite students into Analog circuit design, they were given a hands on exposure to the Cadence Design Environment. This was primarily attended by M.Tech. VLSI Design (1st year) students.